REMARKS

Applicants respectfully request the Examiner to examine the claims requested above.

VERSION MARKED TO SHOW CHANGES

IN THE CLAIMS

Please add claims 23-46.

1   23.   (New) A method comprising:

2   (A) altering an address by masking a portion of the address having lesser significance

3   than a least significant bit of the address containing a logic one value;

4   (B) performing an operation on at least the masked address to produce an index;

5   (C) obtaining information from a forwarding mechanism being addressed using the

6   index;

7   (D) comparing the information with the masked address; and

8   (E) repeating (A-D) if the information does not match the masked address.

1   24.   (New) The method of claim 23, wherein the index is produced by a hash

2   operation on the masked address.

1   25.   (New) The method of claim 24, wherein the obtaining information from a

2   forwarding mechanism includes obtaining information from a bin of a hash table.

1   26.   (New) The method of claim 23, wherein the address is a destination Internet

2   Protocol (IP) address.

1   27.   (New) The method of claim 23, wherein the address is a source Internet Protocol

2   (IP) address.

1   28.   (New) The method of claim 23, wherein the altering of the address includes
2   providing a mask associated with the address and performing a bitwise AND of the address and
3   the mask to produce the masked address.

1   29.   (New) The method of claim 28, wherein the comparing of the information with
2   the masked address further comprises:
3   determining if a length of the mask associated with the address is greater than a
4   predetermined threshold; and
5   performing (E) if the length of the mask is greater than the predetermined threshold and
6   the information fails to match the masked address.

1   30.   (New) The method of claim 23, wherein the altering of the address by masking
2   includes
3   providing a mask of length X-Y, where X represents a bit position of the most significant
4   bit in the address and Y represents a bit position of the least significant bit containing a one; and
5   applying the mask to the address.

1   31.   (New) A method of forwarding data comprising:
2   (A) receiving a search key;
3   (B) producing a masked search key by applying a mask to a portion of the search key
4   starting at a least significant bit of the search key until a least significant bit of the masked search
5   key containing a logic one value;
6   (C) performing a hash function on the masked search key to produce an index;
7   (D) comparing information stored within a bin of a forwarding mechanism, the bin being
8   addressed by the index;
9   (E) determining whether a length of the mask is greater than a predetermined threshold
10 concurrently with (C) and (D).
11 (F) repeating (B-E) for another search iteration if the information does not match the
12 masked search key and the length of the mask is greater than the predetermined threshold.

1 32. (New) The method of claim 31, wherein, for each search iteration, the mask of

2 the search key is shortened to exclude enough data to cause the hash function to produce a result

3 that is different from a former index.

1 33. (New) The method of claim 32, wherein the shortening of the mask comprises,

2 for each search iteration, shortening the length of the mask to X-Y bits, where X represents the

3 bit position of a most significant bit in the search key and Y represents a bit position of the least

4 significant bit in the search key containing a logic one value.

1 34. (New) The method of claim 31, wherein the search key comprises a destination

2 Internet Protocol (IP) address.

1 35. (New) An address relocation unit for improving a longest match search,

2 comprising:

3 a hash table including a plurality of bins;

4 a hash generator to produce an index from an input address and a mask, the index being

5 used to recover data stored in a first bin of the plurality of bins;

6 circuitry to determine whether the data recovered from the first bin compares with the

7 address; and

8 a mask decimation logic coupled to the hash generator, the mask decimation logic to

9 shorten the mask supplied to the hash generator so that the hash generator produces a new index

10 that differs from the index if the data recovered fails to compare with a portion of the address

11 identified by the mask.

1 36. (New) The address resolution unit of claim 35, wherein the circuitry comprises

2 a storage element coupled to the hash table, the storage element to store the data retrieved

3 from the hash table; and

4 a matching logic coupled to the storage element, the matching logic to compare the

5 portion of the address identified by the mask with the data retrieved from the hash table.

1   37.   (New) The address resolution unit of claim 36, wherein the storage element

2 includes a plurality of registers.

1   38.   (New) The address resolution unit of claim 36 further comprising:

2   a state machine coupled to the matching logic and the hash generator, the state machine to

3 control generation of the new index by the hash generator.

1   39.   (New) The address resolution unit of claim 35, wherein operations of the

2 matching logic are independent of the data retrieved from the hash table.

1   40.   (New) The address resolution unit of claim 35 being implemented as part of an

2 input/output (I/O) interface within a switching device.

1   41.   (New) A method of locating an entry in a forwarding database corresponding to a

2 longest match of a search key, the method comprising:

3   performing a function on the search key to produce a current index;

4   searching a first location in a table identified by the current index for an entry that

5 matches the search key; and

6   if no entry is found that matches the search key, each subsequent search iteration

7 performing the following:

8   shortening the search key to exclude data to cause the function to produce a result

9   that differs from the current index,

10   updating the current index with the result, and

11   searching a second location in the table that is identified by the current index.

1   42.   (New) The method of claim 41, wherein the performing of the function includes

2 the performing of a hash function.

1   43.   (New) The method of claim 41, wherein each subsequent search iteration being

2 performed if no entry is found that matches a length of the search key is greater than a

3 predetermined length.

44.   (New) The method of claim 43, wherein the searching of the first location includes searching of a bin of a plurality of bins associated with the table.

45.   (New) A method of locating an entry in a forwarding database corresponding to a longest match of a search key, the method comprising:
   generating indices for a hash table performing a hash function on the search key to produce a current index into a hash table;
   searching a first bin in the hash table identified by the current index for an entry that matches the search key; and
   if no entry has been found that matches the search key, for each subsequent search iteration:
       shortening the search key to exclude data to cause the hash function to produce a result that is different than the current index,
       updating the current index with the result, and
       searching a different bin in the hash table that is identified by the current index.

46.   (New) The method of claim 44, wherein each subsequent search iteration being performed if no entry is found that matches a length of the search key is greater than a predetermined length.

CONCLUSION

In view of the amendments and remarks made above, it is respectfully submitted that

pending claims 23-46 are in condition for allowance, and such action is respectfully solicited.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

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WILLIAM W. SCHAAL
Reg. No. 39,018

12400 Wilshire Boulevard, Seventh Floor
Los Angeles, California 90025
(714) 557-3800

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on: July 6, 2001.

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Corrinn R. Reynolds